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APPLICANTS

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** FOREIGN APPLICATIONS *****

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35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance Verified and Acknowledged <u>EP GARCIA-OPREX</u> <u>EG</u> Initials Examiner's Signature Initials				

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TITLE

Semiconductor device simulation method and simulator

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